

52-536

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10080119	02/21/2002	257		2618	
**APPLICANTS: Powell Don; Mercaldi Garry.					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/653,096 08/31/2000 <i>MP</i>					
**FOREIGN APPLICATIONS VERIFIED: <i>None</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				MIO 0060 VAV98-0814.01	
Verified and Acknowledged Examiners's initials <i>MP</i>					
TITLE : Method for forming a dielectric layer and semiconductor device incorporating the same					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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